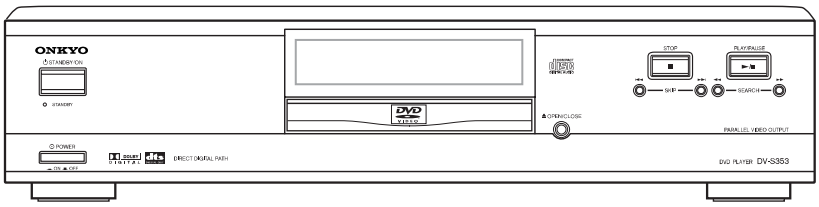
Ref. No.3693
072001

ONKYO® SERVICE MANUAL

DVD Player

DV-S353



Black model

UDD	120V AC, 60Hz

RC-458DV

SAFETY-RELATED COMPONENT WARNING!!

COMPONENTS IDENTIFIED BY MARK ⚠ ON THE SCHEMATIC DIAGRAM AND IN THE PARTS LIST ARE CRITICAL FOR RISK OF FIRE AND ELECTRIC SHOCK. REPLACE THESE COMPONENTS WITH ONKYO PARTS WHOSE PART NUMBERS APPEAR AS SHOWN IN THIS MANUAL.

MAKE LEAKAGE-CURRENT OR RESISTANCE MEASUREMENTS TO DETERMINE THAT EXPOSED PARTS ARE ACCEPTABLY INSULATED FROM THE SUPPLY CIRCUIT BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

ONKYO®
IMAGINATIVE SIGHT & SOUND

SPECIFICATION

■ DVD Player

Power supply	AC 120 V, 60 Hz	
Power consumption	16 W	
Weight	8.2 lbs	
External dimensions	17 1/8" x 3 7/8" x 12 11/16" (W/H/D)	
Signal system	NTSC	
Frequency range (digital audio)	DVD linear sound:	48 kHz sampling 4 Hz to 22 kHz 96 kHz sampling 4 Hz to 44 kHz
Signal-to-noise ratio (digital audio)	More than 95 dB	
Audio dynamic range (digital audio)	More than 95 dB	
Harmonic distortion (digital audio)	Less than 0.005 %	
Wow and flutter	Below measurable level	
Operating conditions	Temperature: 5˚C to 35˚C (41˚F to 95˚F), Operation status: Horizontal	

■ Outputs

Video output	1.0 V (p-p), 75 ohm, negative sync., pin jack × 1
S-video output	(Y) 1.0 V (p-p), 75ohm, negative sync., Mini DIN 4-pin × 1 (C) 0.286 V (p-p), 75ohm
Component video output	(Y) 1.0 V (p-p), 75ohm, negative sync., pin jack × 1 (P_B)/(P_R) 0.7 V (p-p), 75ohm pin jack × 2
Audio output (digital output Optical)	Optical connecter × 1
Audio output (digital output Coaxial)	0.5 V (p-p), 75ohm, pin jack × 1
Audio output (2-Channel Audio)	2.0 V (rms), 320ohm, pin jack (L, R) × 1

■ Supplied Accessories

Audio/video connection cable	1
Remote controller (RC-458DV)	1
Batteries (size AAA/R3)	2

Specifications and features are subject to change without notice.

SERVICE NOTES

WARNING

WARNING:
TO REDUCE THE RISK OF FIRE OR ELECTRIC SHOCK, DO NOT EXPOSE THIS APPLIANCE TO RAIN OR MOISTURE.

CAUTION:
TO REDUCE THE RISK OF ELECTRIC SHOCK, DO NOT REMOVE COVER (OR BACK). NO USER-SERVICEABLE PARTS INSIDE. REFER SERVICING TO QUALIFIED SERVICE PERSONNEL.

The lightning flash with arrowhead symbol, within an equilateral triangle, is intended to alert the user to the presence of uninsulated ìdangerous voltageî within the productís enclosure that may be of sufficient magnitude to constitute a risk of electric shock to persons.

The exclamation point within an equilateral triangle is intended to alert the user to the presence of important operating and maintenance (servicing) instructions in the literature accompanying the appliance.

LASER CAUTION

This unit contains a semiconductor laser system and is classified as a "CLASS 1 LASER PRODUCT". So, to use this model properly, read this Instruction Manual carefully. In case of any trouble, please contact the store where you purchased the unit. To prevent being exposed to the laser beam, do not try to open the enclosure.

CAUTION:
VISIBLE LASER RADIATION WHEN OPEN AND INTERLOCK FAILED OR DEFEATED. DO NOT STARE INTO BEAM.

CAUTION:
THIS PRODUCT UTILIZES A LASER. USE OF CONTROLS OR ADJUSTMENTS OR PERFORMANCE OF PROCEDURES OTHER THAN THOSE SPECIFIED HEREIN MAY RESULT IN HAZARDOUS RADIATION EXPOSURE.

The label on the right is applied on the rear panel except for USA and Canadian models.

1. This unit is a CLASS 1 LASER PRODUCT and employs a laser inside the cabinet.
2. To prevent the laser from being exposed, do not remove the cover. Refer servicing to qualified personnel.

LASER DIODE

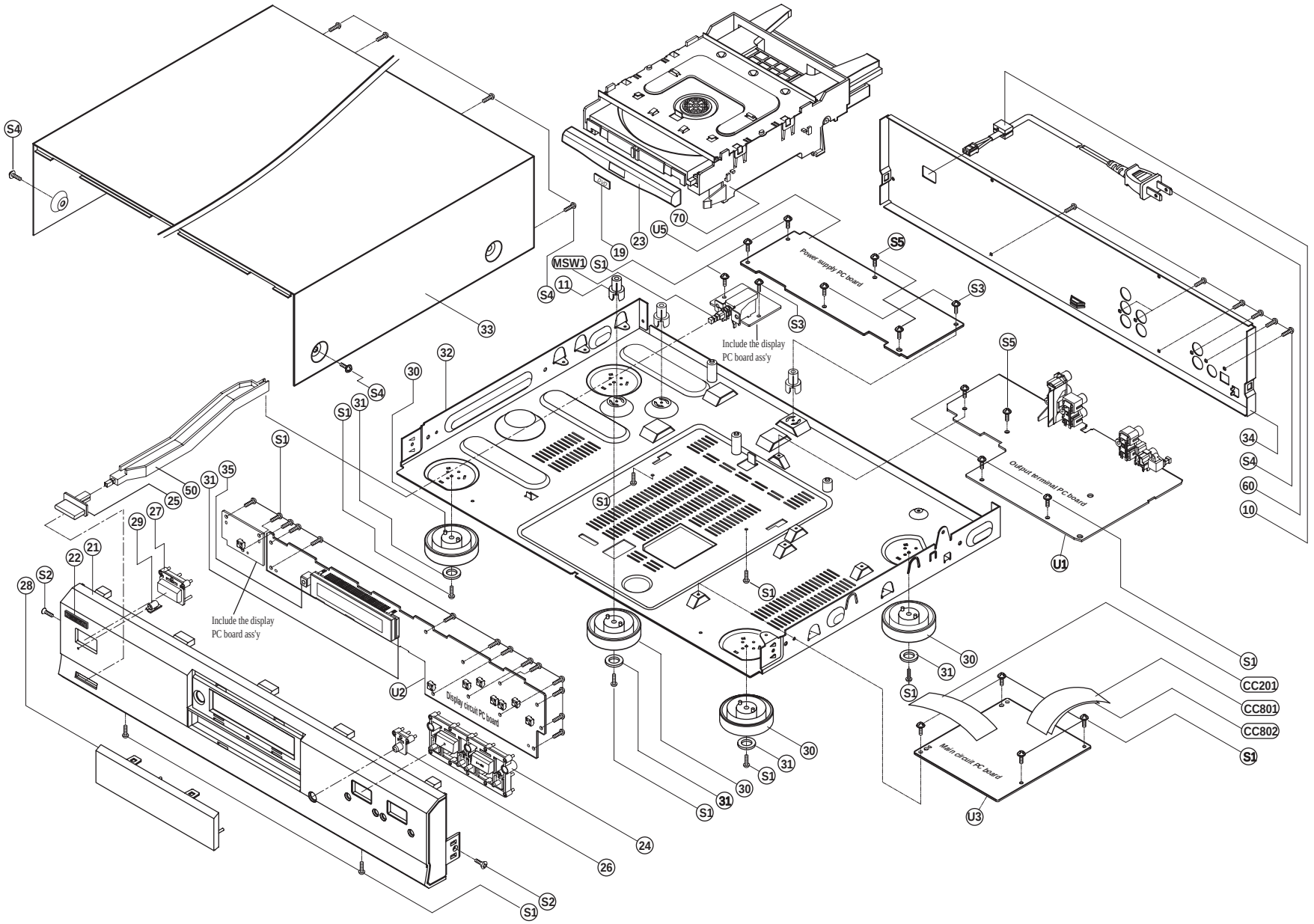
Never look at the laser light beam through the objective lens. The visible laser beam may harm the human eye. Use an appropriate viewer or TV camera to observe the laser beam.

Any laser diode will be damaged by a large current or pulse. Avoid current surges and electrostatic discharges. Safety grounding of the human body and all measuring equipment, jigs and tools is absolutely necessary. The use of a grounding mat on the workbench and floor is necessary. Even a small electrostatic discharge from the human body may destroy the laser diode instantaneously or reduce its life time.

The pins of the laser diode may be shorted at the factory by a solder bridge at the FPC for protection during transportation and storage of the unit. If this is the case, open the short circuit (accessible from the bottom side of the module) quickly with grounded soldering iron (less than 300ÞC).

The damage of the diode might be indicated by an increase of the laser current.

EXPLODED VIEW

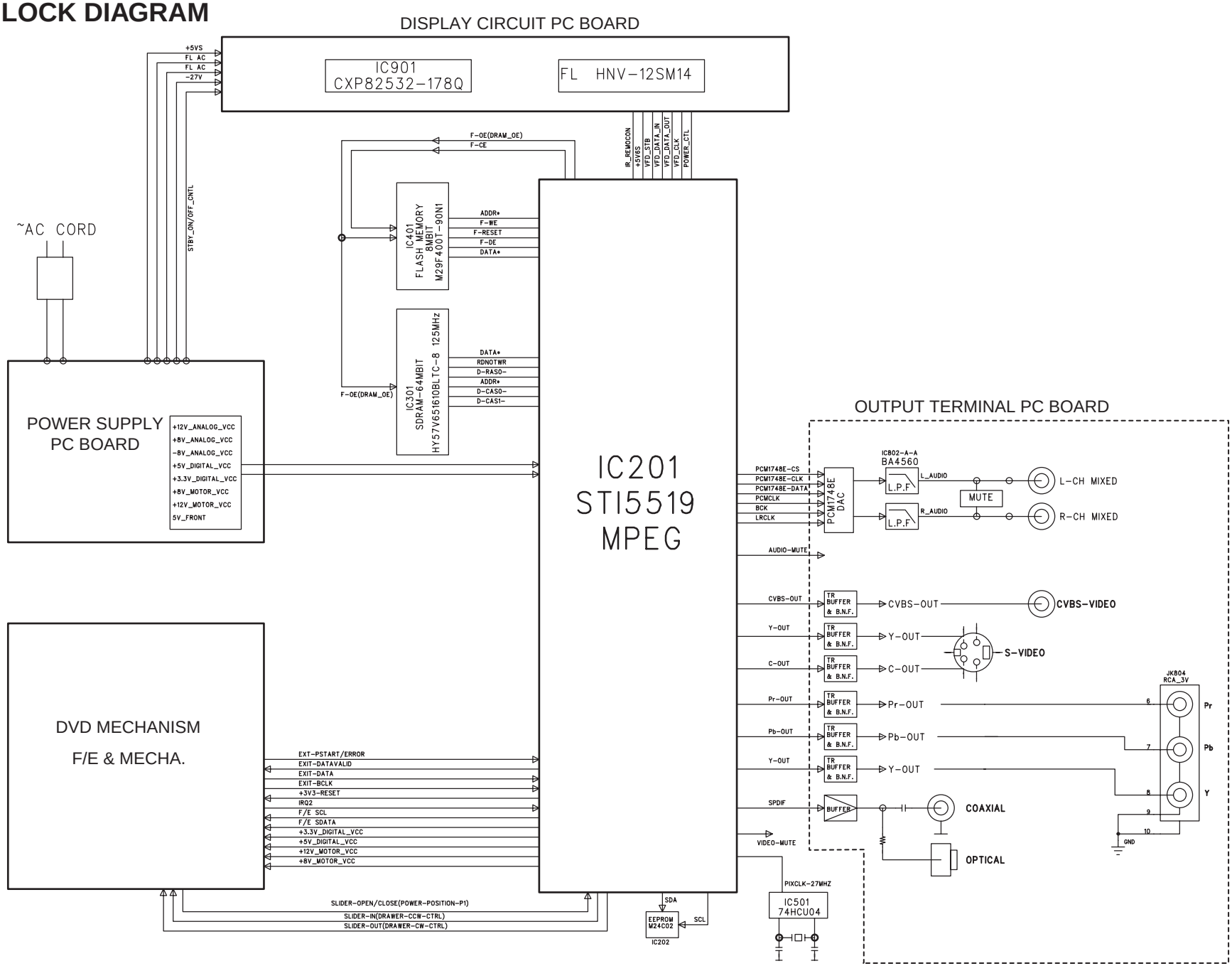


PARTS LIST (Exploded view)

REF. NO.		PART NO.	DESCRIPTION
U1		JE010073001A	Output terminal PC board ass'y
U2		JE010069001A	Display circuit PC board ass'y
U3		JE010063001A	Main circuit PC board ass'y
U5	⚠	J4090010150X	Power supply PC board ass'y
10	⚠	J65100000100	Cord bushing
11		J85820001100	Spacer, PC board
13		J67300026000	3.5MM, sponge
19		J60550012000	Badge, DVD
20		20370190XX	Wire tie
21		J85000034000	Front panel
22		J85510003000	Badge, ONKYO
23		J85600024000	Door tray
24		J85200158000	Button, PLAY
25		J85200160000	Knob, POWER
26		J85200157000	Button, OPEN/CLOSE
27		J85200159000	Button, POWER
28		J85300036000	Clear plate
29		J85400058000	Facet, Standby ind.
30		55125120XX	Leg (Rear)
31		55141370XX	Rubber, leg
32		J60000005300	Chassis
33		J60100015000	Top cover
34		J60110018000	Rear panel
35		J67200019000	Cover, Sensor
50		J85820004000	Shaft power
60	⚠	J43730100100	Power cord
70		10675110XX	DVD mechanism ass'y, TVM 502 C
CC201		J4112191201X	Flexible Flat cable,19P 120mm 1.25mm
CC801		J4112250901X	Flexible Flat cable,25P 90mm 1.25mm
CC802		J4112150601X	Flexible Flat cable,15P 60mm 1.25mm
S1		833130088	3TTB+8B, Self tapping screw
S2		55127180XX	3 x 8, Flat head bind screw
S3		55127290XX	3 x 18, Bind screw
S4		838430088	3TTB+8(BC), Self tapping screw
S5		J81000300810	3 x 8, Bind screw
MSW1	⚠	J46203000101	Power switch

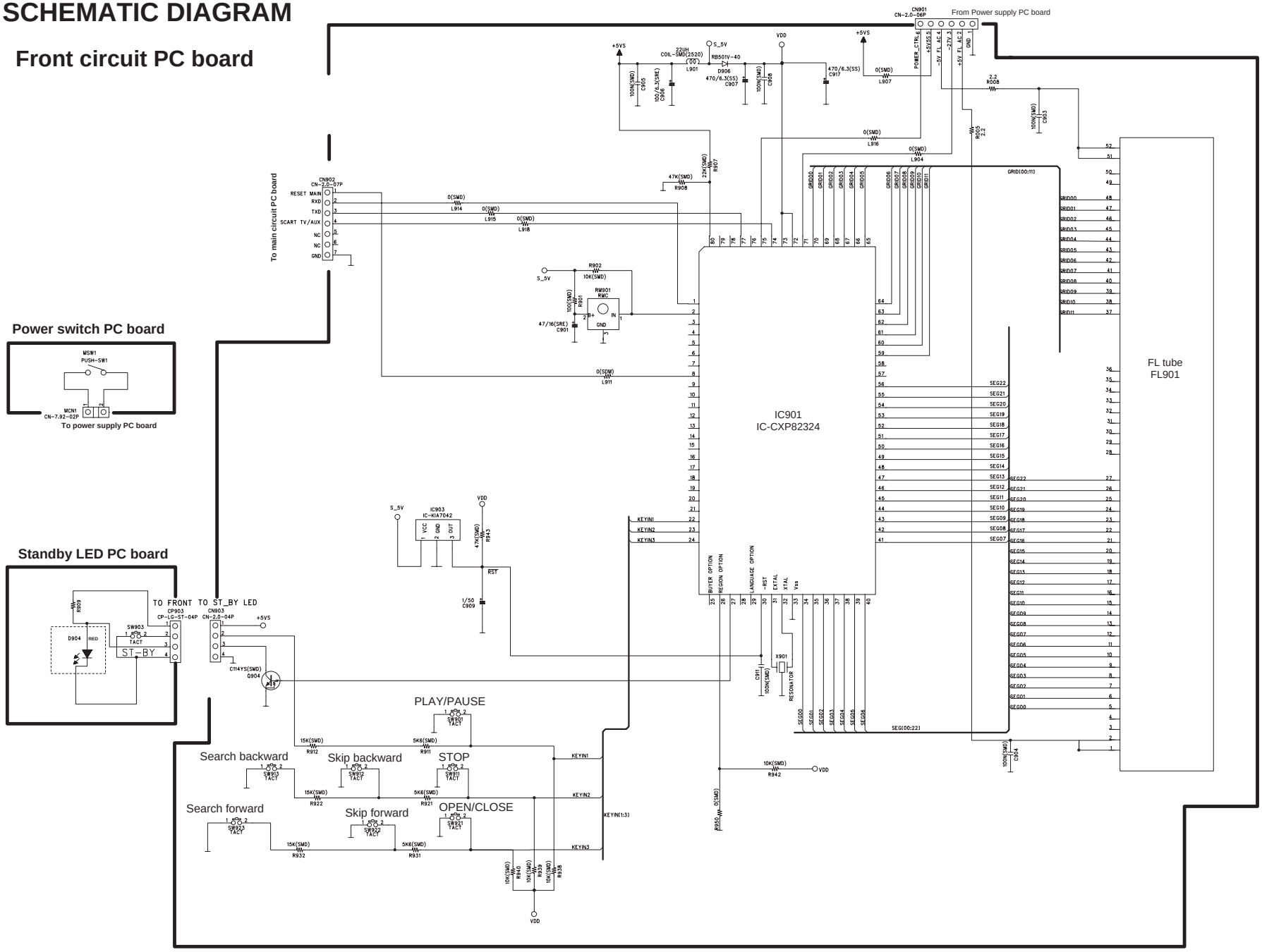
NOTE: THE COMPONENTS IDENTIFIED BY MARK ⚠ ARE CRITICAL FOR RISK OF FIRE AND ELECTRIC SHOCK. REPLACE ONLY WITH PART NUMBER SPECIFIED.

BLOCK DIAGRAM



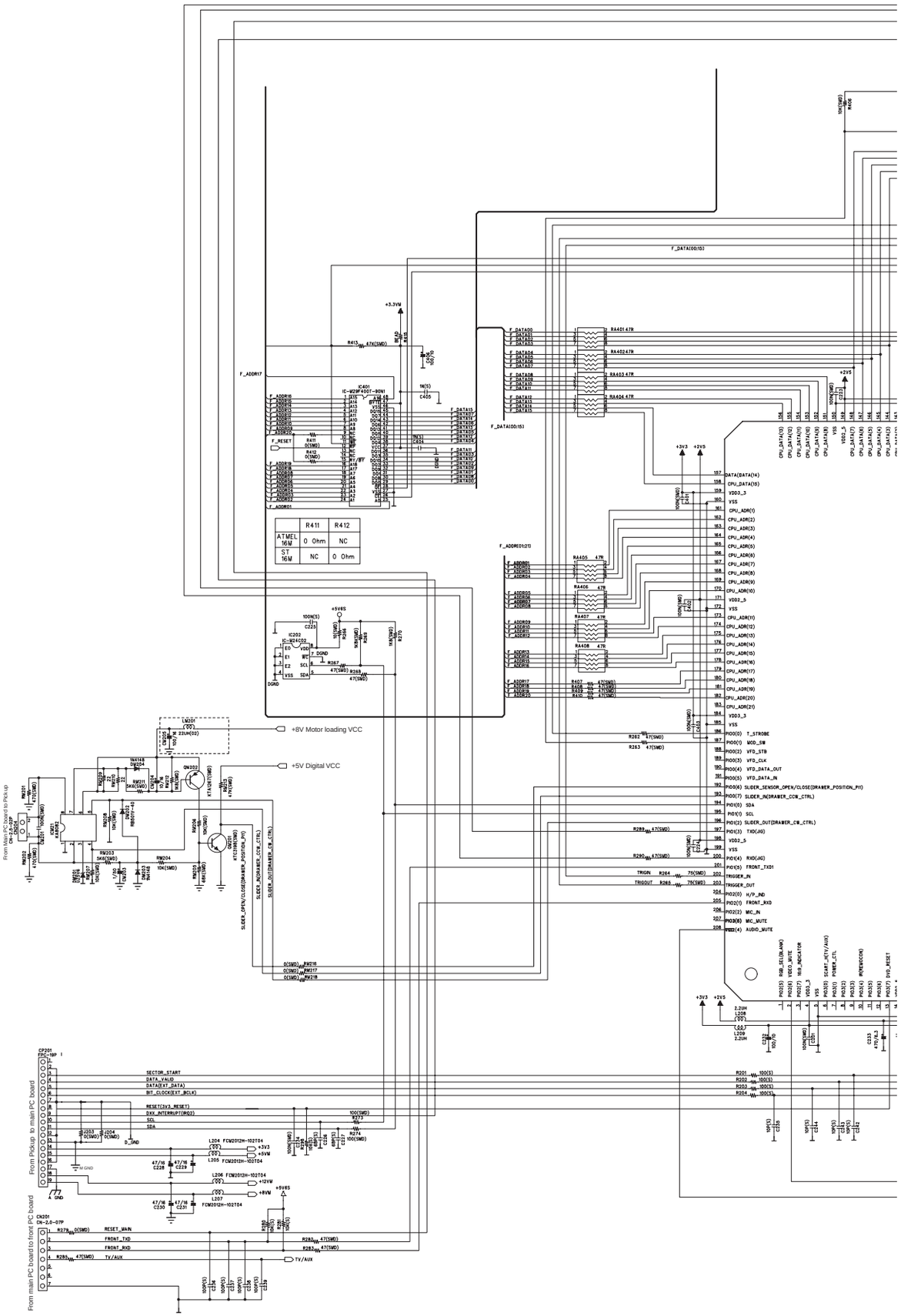
SCHEMATIC DIAGRAM

Front circuit PC board

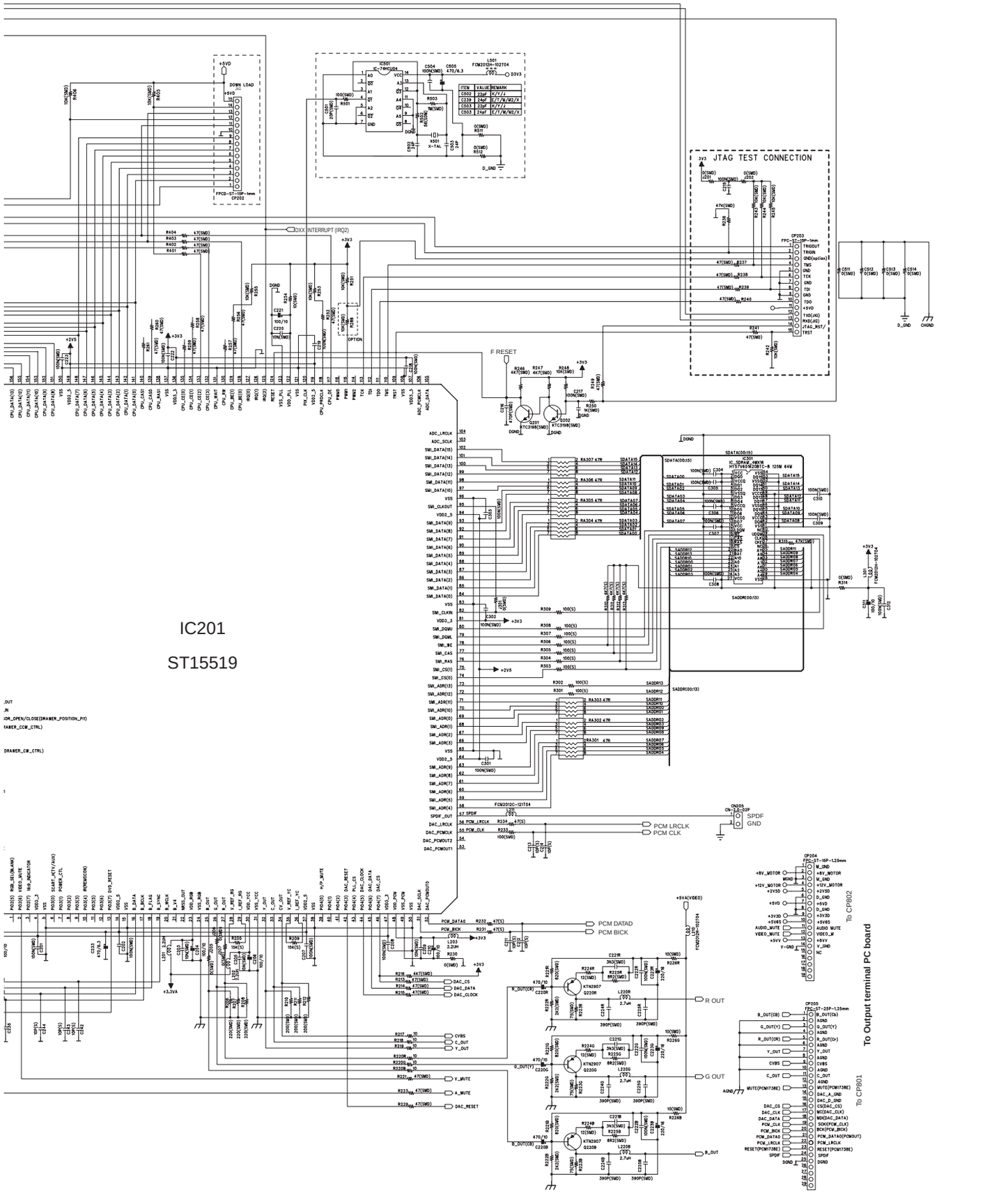


SCHEMATIC DIAGRAM

Main circuit PC board

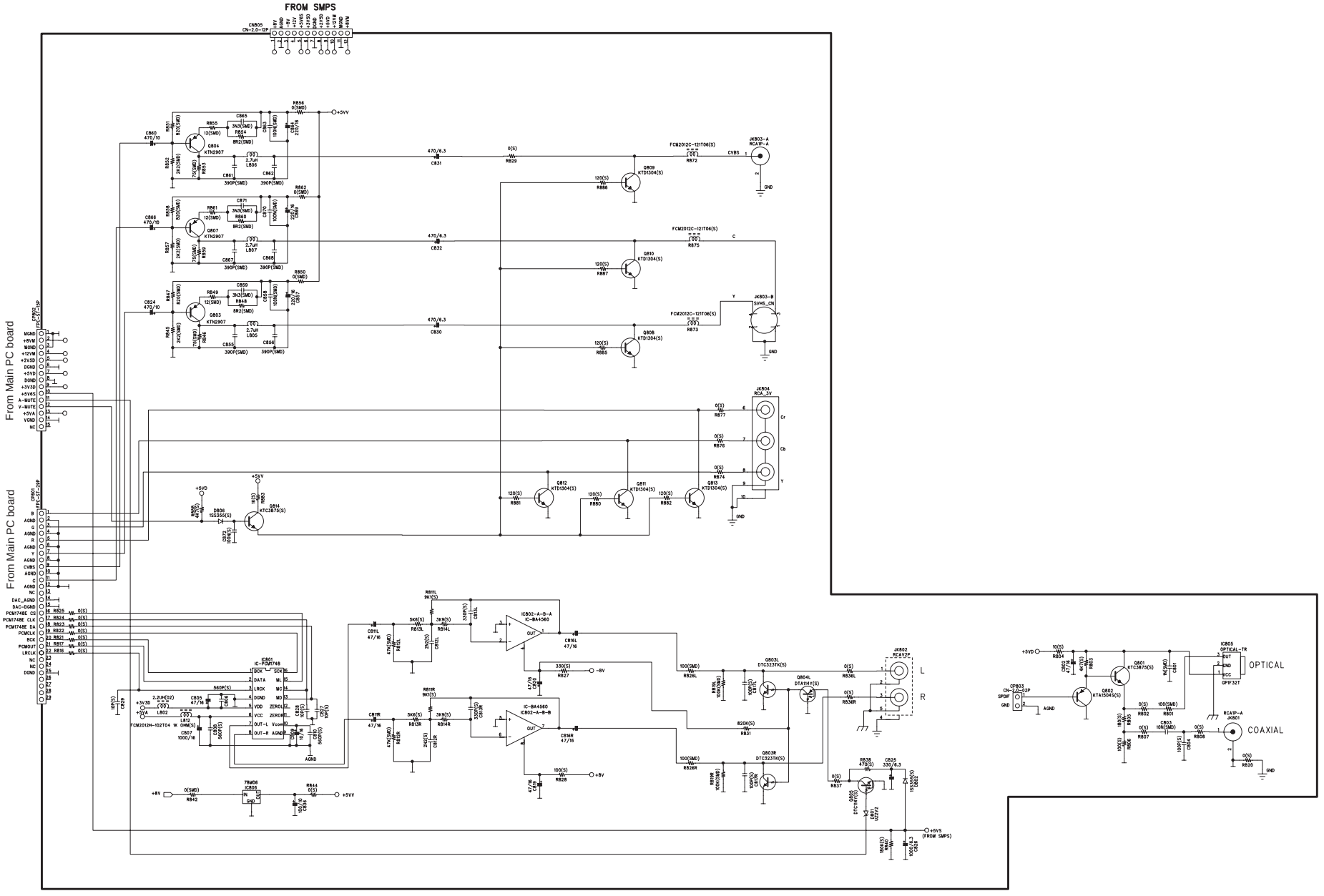


	R411	R412
ATMEL 16M	0 Ohm	NC
ST 16M	NC	0 Ohm

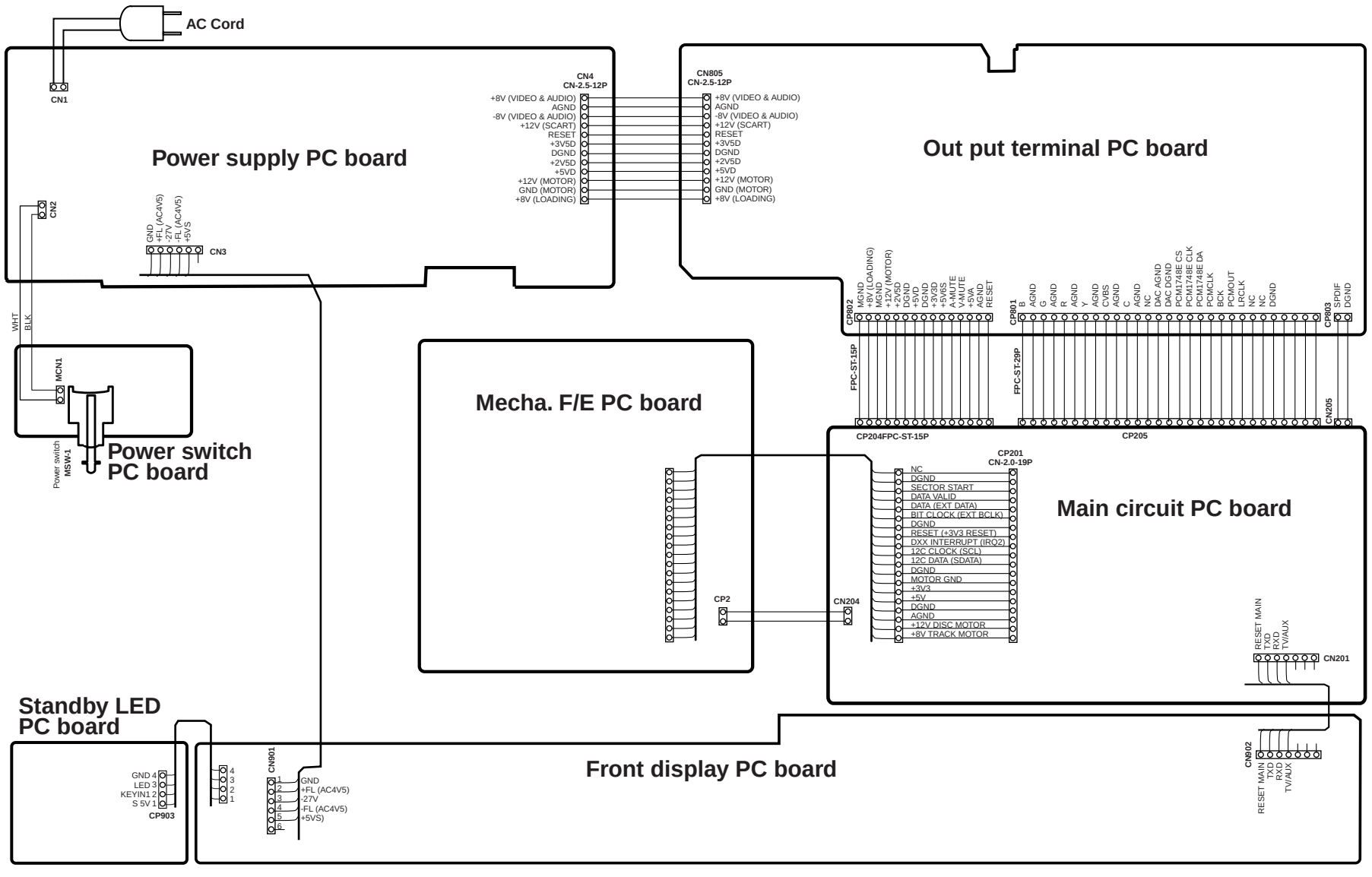

JTAG TEST CONNECTION
IC201
ST15519
PCM LRCLK
PCM CLK
PCM DATAD
PCM BICK
R OUT
G OUT
B_OUT
CVBS
C_OUT
Y_OUT
V_MUTE
A_MUTE
DAC_RESET
DAC_CS
DAC_DATA
DAC_CLOCK
To CP802
To CP801
To Output terminal PC board

SCHEMATIC DIAGRAM

Output terminal PC board



PC BOARD CONNECTION DIAGRAM



PARTS LIST (PRINTED CIRCUIT BOARD)

Main circuit PC board

REF. NO.	PART NO.	DESCRIPTION
	ICs	
IC201	55130350NR	STi5519 Decoder
IC202	20940740NR	M24C02-MN6 EEPROM
IC301	55156290NR	HY57V651620BLTC-8 DRAM
IC301	55156420NR	KS641632D DRAM
IC401	55136720NR	M29W800AT-90N1 Flash
IC501	55133310NR	74HCU04M1R Inverter
ICM21	J2113232001X	KA8082 Motor driver
	Transistors	
Q201,Q202, QM201	2097046092	KTC3875S
Q220B,Q220G, Q220R	5513869056	KTN2907A
QM202	2097048092	KTA1504S Chip
	Resonator	
X501	55128980NR	27.000MHz 16.5PF HC-49/S
	Diodes	
DM201	2041428016	MTZ J 5.6B
DM202	J2221510304X	RB441Q-40 Schottky
DM203,DM204	7043654016	1N4148
	Coils	
L204-L207, L210	5512667050	FCM2012H-102T04 1kohm
L220B,L220G, L220R	1404175091	2.7uH Chip
L501,R415	5512667050	FCM2012H-102T04 1kohm
	Sockets	
CP201	55124600XX	19P 1.25mm ST GF120-19S-TS
CP202,CP203	55125910XX	1.0mm 15P 00-6232-015-006-800
CP204	55124580XX	15P 1.25mm ST GF120-15S-TS
CP205	55124630XX	25P 1.25mm ST GF120-25S-TS
	Plugs	
CN204	J4420040271X	
	Sockets AS	
CN205	J4335300105X	2P 250MM 2.0mm AGW#26
	Connectors	
CN201	55123330XX	2.0mm ST GIL-S-6P-S2T2
	Capacitors	
C204,C206, C210,C221, C232,C311, C406	2026894030	100uF,10V+/-20%, 5 x 11
C220B,C220G, C220R	2025267030	470uF,10V+/-20%, 5 x 11
C223B,C223G, C223R	2025265030	220uF,16V+/-20%, 8 x 11.5
C228-C231	2026783030	47uF,16V+/-20%, 5 x 11
C233,C505	2026862030	470uF,6.3V+/-20%,
CM203	2026884030	1uF,50V+/-20%, 5 x 11
CM204	1500213030	10uF,16V+/-20%, 5 x 11
CM205	2026902030	100uF,16V+/-20%

Display circuit PC board

REF. NO.	PART NO.	DESCRIPTION
	FL tube	
FL901	55151330NR	HNV-12SM14
	Remote sensor	
RM901	J2411320014X	RPM6938524 -38kHz
	Resonator	
X901	55126140NR	10.0MHZ
	ICs	
IC901	55151320NR	CXP82532-187Q, Microprocessor
IC903	J2112505044X	KIA7045P 4.5V, Reset
	Transistor	
Q904	5505124092	KRC107S NPN
	LED	
D904	55125510NR	LTL-1CHE3
	Diode	
D906	J2242510011X	RB510V-40 Schottky
FIG90,FIG92	J65540001200	
	Coil	
L901	1403936091	22uH, Chip
	Capacitors	
C901	2026783041	47uF,16V+/-20%, 5 x 7
C906	2025205041	100uF,6.3V+/-20%, 5 x 7
C907,C917	2026862041	470uF,6.3V+/-20%, 8 x 9
C909	J3470910970X	1uF,50V+/-20%, 5 x 7
	Sockets AS	
CN901	J4300200104X	6P 300MM
CN902	J4300200107X	6P 80MM
MCN1	J4300200108X	
CN903	J4335300101X	4P 160MM 2.0MM
	Resistors	
L905,L906	J3013100320X	10 ohm, 1/5W,Metal oxide
	Switches	
MSW1	J46203000101	SDDLB14700, Power switch
SW901,SW903, SW911-SW913, SW921-SW923	J46500500501	SKQNAE, Tact
	Others	
050	J67300026000	3.5MM, sponge
060	J67300025000	6MM, Sponge for sensor

DVD SOFTWARE DOWNLOAD

1. Connect the printer cable to your PC printer port and connected with down load jig.
2. Set the PC mode into the DOS mode.
3. Copy the driver program 'DVD_DOWN.EXE' and main program '353v×××.bin' into the same directory(Ex. DVDDL) then move into the directory.

(3 numeric ××× means version number)

4. Insert the FFC cable to the CP202 silkscreened 'DOWN LOAD' on the main Board.

5.Turn on the product and make sure the LED on the jig will be light.

6. type 'DVD_DOWN 353v×××.bin' then you will see the message below

file size 1897904

word size 978952

Do you want to program FLASH MEMORY? (y/n).

Then press 'y'

It'll takes 2 min to be complete down load.

To check embed program version.

Press 'FR ' and 'standby-on' button at the same time.

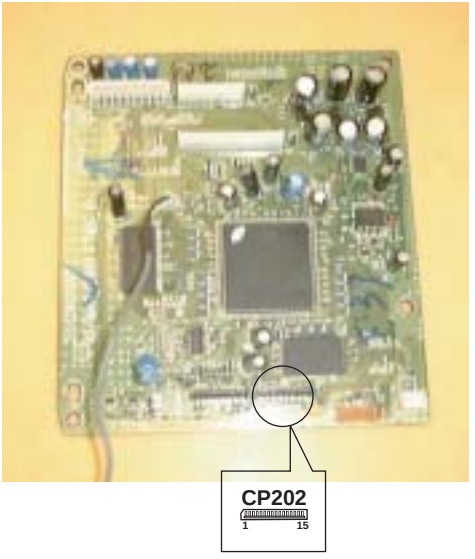
Then fl display below.

7×××○○○ 7 : (fixed number means ONKYO)

××× ; Version Number

○○; Compile date.

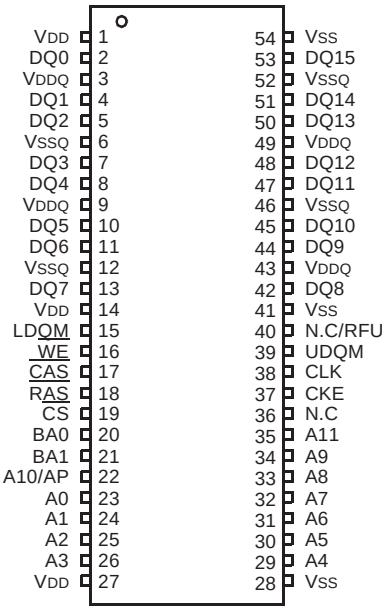
Main circuit PC board



IC TERMINAL DESCRIPTION

K4S641632D (SDRAM)

PIN CONFIGURATION (Top view)



PIN FUNCTION DESCRIPTION

Pin	Name	Input Function
CLK	System clock	Active on the positive going edge to sample all inputs.
$\overline{CS}$	Chip select	Disables or enables device operation by masking or enabling all inputs except CLK, CKE and L(U)DQM
CKE	Clock enable	Masks system clock to freeze operation from the next clock cycle. CKE should be enabled at least one cycle prior to new command. Disable input buffers for power down in standby.
A_0 ~ A_{11}	Address	Row/column addresses are multiplexed on the same pins. Row address : RA_0 ~ RA_{11}, Column address : CA_0 ~ CA_7
BA_0 ~ BA_1	Bank select address	Selects bank to be activated during row address latch time. Selects bank for read/write during column address latch time.
$\overline{RAS}$	Row address strobe	Latches row addresses on the positive going edge of the CLK with $\overline{RAS}$ low. Enables row access & precharge.
$\overline{CAS}$	Column address strobe	Latches column addresses on the positive going edge of the CLK with $\overline{CAS}$ low. Enables column access.
$\overline{WE}$	Write enable	Enables write operation and row precharge. Latches data in starting from $\overline{CAS}$, $\overline{WE}$ active.
L(U)DQM	Data input/output mask	Makes data output Hi-Z, t_{SHZ} after the clock and masks the output. Blocks data input when L(U)DQM active.
$DQ_{0 \sim 15}$	Data input/output	Data inputs/outputs are multiplexed on the same pins.
V_{DD}/V_{SS}	Power supply/ground	Power and ground for the input buffers and the core logic.
V_{DDQ}/V_{SSQ}	Data output power/ground	Isolated power supply and ground for the output buffers to provide improved noise immunity.
N.C/RFU	No connection /reserved for future use	This pin is recommended to be left No Connection on the device.

IC TERMINAL DESCRIPTION

STI5519 Microprocessor terminal description

DV-S353 MAIN IC201 STI5519 PIN LIST

PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
1	PIOs and communication	PIO2[5]	PIO2[5]	I/O
2	PIOs and communication	PIO2[6]	PIO2[6]	I/O
3	PIOs and communication	PIO2[7]	PIO2[7]	I/O
4	-	VDD3_3	3.3V POWER SUPPLY	POWER
5	-	VSS	GROUND	POWER
6	PIOs and communication	PIO3[0]	PIO3[0]	I/O
7	PIOs and communication	PIO3[1]	PIO3[1]	I/O
8	PIOs and communication	PIO3[2]	PIO3[2]	I/O
9	PIOs and communication	PIO3[3]	PIO3[3]	I/O
10	PIOs and communication	PIO3[4]	PIO3[4]	I/O
11	PIOs and communication	PIO3[5]	PIO3[5]	I/O
12	PIOs and communication	PIO3[6]	PIO3[6]	I/O
13	PIOs and communication	PIO3[7]	PIO3[7]	I/O
14	-	VDD2_5	2.5V POWER SUPPLY	POWER
15	-	VSS	GROUND	POWER
16	Front-end	B_DATA	I2S DATA	I
17	Front-end	B_BCLK	I2C BIT CLOCK	I
18	Front-end	B_FLAG	I2S ERROR FLAG DVD	I
19	Front-end	B_SYNC	I2S SECTOR/ABS TIME	I
20	RESERVED	RESERVED	-	I/O
21	RESERVED	RESERVED	-	I/O
22	RESERVED	RESERVED	-	I/O
23	RESERVED	VDD_RGB	VDDA_RGB=2.5V	POWER 2.5V
24	RESERVED	VSS_RGB	VSSA_RGB=GND	POWER GND
25	Video DAC	B_OUT	B_OUT	O
26	Video DAC	G_OUT	G_OUT	O
27	Video DAC	R_OUT	R_OUT	O
28	Video DAC	V_REF_RGB	V_REF_DAC_RGB	I
29	Video DAC	I_REF_RGB	I_REF_DAC_RGB	I
30	Video DAC	VDD_YCC	VDDA_YCC=2.5V	POWER 2.5V
31	Video DAC	VSS_YCC	VSSA_YCC=GND	POWER GND
32	Video DAC	Y_OUT	Y_OUT	O
33	Video DAC	C_OUT	C_OUT	O
34	Video DAC	CV_OUT	CV_OUT	O
35	Video DAC	V_REF_YCC	V_REF_DAC_YCC	I
36	Video DAC	I_REF_YCC	I_REF_DAC_YCC	I
37	-	VDD2_5	2.5V POWER SUPPLY	POWER
38	-	VSS	GROUND	POWER
39	PIOs and communication	PIO4[0]	PIO4[0]	I/O
40	PIOs and communication	PIO4[1]	PIO4[1]	I/O
41	PIOs and communication	PIO4[2]	PIO4[2]	I/O
42	PIOs and communication	PIO4[3]	PIO4[3]	I/O
43	PIOs and communication	PIO4[4]	PIO4[4]	I/O
44	PIOs and communication	PIO4[5]	PIO4[5]	I/O
45	PIOs and communication	PIO4[6]	PIO4[6]	I/O
46	PIOs and communication	PIO4[7]	PIO4[7]	I/O
47	-	VDD3_3	3.3V POWER SUPPLY	POWER
48	AUDIO_DAC	VDD_PCM	VDD FREQ SYNTHE=2.5V	POWER 2.5V
49	AUDIO_DAC	VSS_PCM	VSS FREQ SYNTHE=GND	POWER GND
50	-	VSS	GROUND	POWER
51	AUDIO_DAC	DAC_SCLK	OVER SAMPLING CLK	O
52	AUDIO_DAC	DAC_PCMOUT0	PCM_OUT0	O
53	AUDIO_DAC	DAC_PCMOUT1	PCM_OUT1	I/O
54	AUDIO_DAC	DAC_PCMOUT2	PCM_OUT2	O
55	AUDIO_DAC	DAC_PCMCLK	PCM_CLOCK	I/O
56	AUDIO_DAC	DAC_LRCLK	LEFT/RIGHT CLK	O
57	AUDIO_DAC	SPDIF_OUT	SPDIF_OUT	O

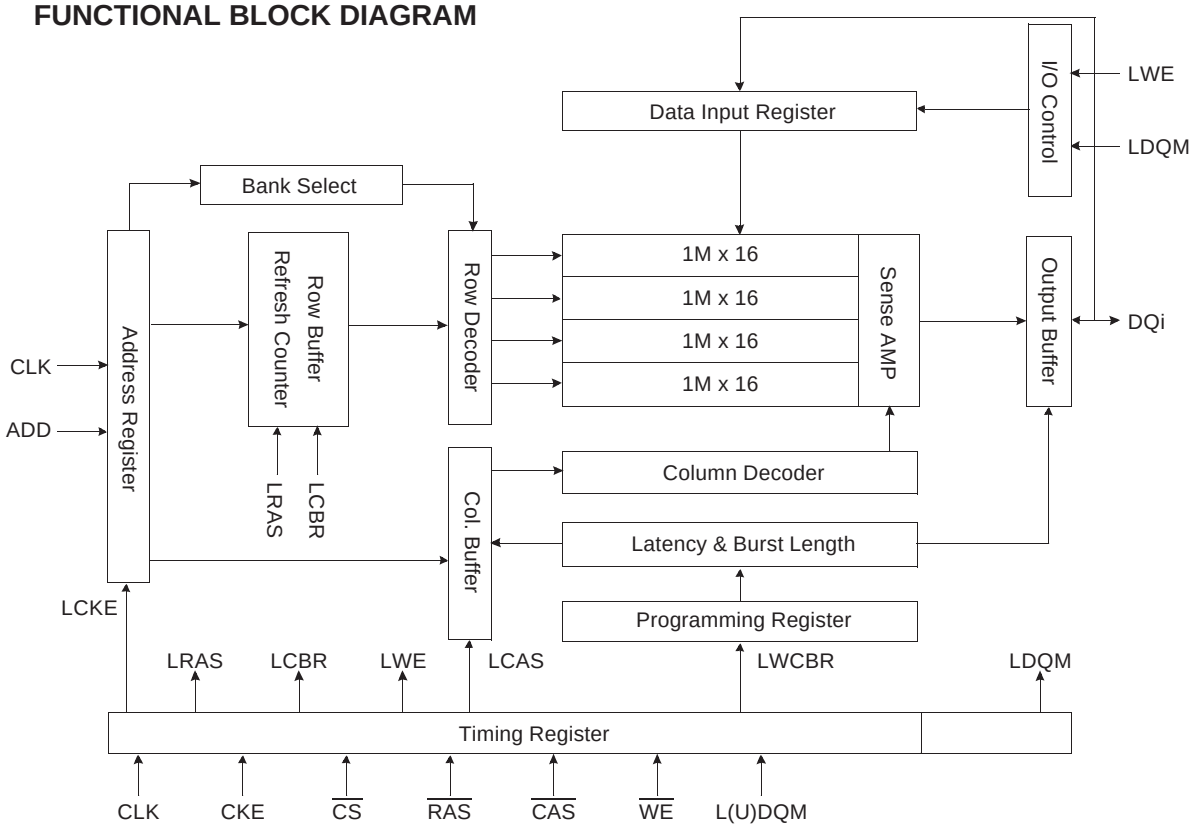
PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
58	Share memory interface	SMI_ADR[4]	ADDRESS BUS SDRAM	O
59	Share memory interface	SMI_ADR[5]	ADDRESS BUS SDRAM	O
60	Share memory interface	SMI_ADR[6]	ADDRESS BUS SDRAM	O
61	Share memory interface	SMI_ADR[7]	ADDRESS BUS SDRAM	O
62	Share memory interface	SMI_ADR[8]	ADDRESS BUS SDRAM	O
63	Share memory interface	SMI_ADR[9]	ADDRESS BUS SDRAM	O
64	-	VDD2_5	2.5V POWER SUPPLY	POWER
65	-	VSS	GROUND	POWER
66	Share memory interface	SMI_ADR[0]	ADDRESS BUS SDRAM	O
67	Share memory interface	SMI_ADR[1]	ADDRESS BUS SDRAM	O
68	Share memory interface	SMI_ADR[2]	ADDRESS BUS SDRAM	O
69	Share memory interface	SMI_ADR[3]	ADDRESS BUS SDRAM	O
70	Share memory interface	SMI_ADR[10]	ADDRESS BUS SDRAM	O
71	Share memory interface	SMI_ADR[11]	ADDRESS BUS SDRAM	O
72	Share memory interface	SMI_ADR[12]	ADDRESS BUS SDRAM	O
73	Share memory interface	SMI_ADR[13]	ADDRESS BUS SDRAM	O
74	Share memory interface	SMI_CS[0]	CHIP SELECT BANK0	O
75	Share memory interface	SMI_CS[1]	CHIP SELECT BANK1	O
76	Share memory interface	SMI_RAS	RAS SDRAM	O
77	Share memory interface	SMI_CAS	CAS SDRAM	O
78	Share memory interface	SMI_WE	SDRAM WRITE ENABLE	O
79	Share memory interface	SMI_DQML	DQ MASK EN LOW	O
80	Share memory interface	SMI_DQMU	DQ MASK EN UP	O
81	-	VDD3_3	3.3V POWER SUPPLY	POWER
82	Share memory interface	SMI_CLKIN	SDRAM CLOCK IN	O
83	-	VSS	GROUND	POWER
84	Share memory interface	SMI_DATA[0]	DATA BUS SDRAM	I/O
85	Share memory interface	SMI_DATA[1]	DATA BUS SDRAM	I/O
86	Share memory interface	SMI_DATA[2]	DATA BUS SDRAM	I/O
87	Share memory interface	SMI_DATA[3]	DATA BUS SDRAM	I/O
88	Share memory interface	SMI_DATA[4]	DATA BUS SDRAM	I/O
89	Share memory interface	SMI_DATA[5]	DATA BUS SDRAM	I/O
90	Share memory interface	SMI_DATA[6]	DATA BUS SDRAM	I/O
91	Share memory interface	SMI_DATA[7]	DATA BUS SDRAM	I/O
92	Share memory interface	SMI_DATA[8]	DATA BUS SDRAM	I/O
93	Share memory interface	SMI_DATA[9]	DATA BUS SDRAM	I/O
94	-	VDD2_5	2.5V POWER SUPPLY	POWER
95	Share memory interface	SMI_CLKOUT	SDRAM CLOCK OUT	O
96	-	VSS	GROUND	POWER
97	Share memory interface	SMI_DATA[10]	DATA BUS SDRAM	I/O
98	Share memory interface	SMI_DATA[11]	DATA BUS SDRAM	I/O
99	Share memory interface	SMI_DATA[12]	DATA BUS SDRAM	I/O
100	Share memory interface	SMI_DATA[13]	DATA BUS SDRAM	I/O
101	Share memory interface	SMI_DATA[14]	DATA BUS SDRAM	I/O
102	Share memory interface	SMI_DATA[15]	DATA BUS SDRAM	I/O
103	RESERVED	RESERVED		I/O
104	RESERVED	RESERVED		I/O
105	RESERVED	RESERVED		I/O
106	RESERVED	RESERVED		O
107	-	VDD3_3	3.3V POWER SUPPLY	POWER
108	-	VSS	GROUND	POWER
109	JTAG	TRAT4	TEST RESET	I
110	JTAG	TMS	TEST MODE SELECT	I
111	JTAG	TDO	TEAT DATA OUT	O
112	JTAG	TDI	TEST DATA IN	I
113	JTAG	TCK	TEST CLOCK	I
114	TIMER	PWM2	PULSE WITDH MODULA2	I/O
115	TIMER	PWM1	PULSE WITDH MODULA1	I/O
116	TIMER	PWM0	PULSE WITDH MODULA0	I/O

PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
117	EMI Interface	CPU_OE	OUTPUT_ENABLE	I/O
118	EMI Interface	CPU_RAM_CLK	SDRAM CLOCK	O
119	-	VDD2_5	2.5V POWER SUPPLY	POWER
120	CLOCK & RESET	PIX_CLK	27 MHz main clock	I
121	-	VSS	GROUND	POWER
122	CLOCK & RESET	VDD_PLL	VDD PLL=2.5V	POWER 2.5V
123	CLOCK & RESET	VSS_PLL	GND PLL=GND	POWER GND
124	CLOCK & RESET	RESET	CHIP RESET	I
125	INTERRUPT	IRQ2[2]	IRQ[2](MD_IRQ)	I
126	INTERRUPT	IRQ2[1]	IRQ[1](ATAPI IRQ)	I
127	INTERRUPT	IRQ2[0]	IRQ[0](SERVO_IRQ)	I
128	EMI Interface	CPU_BE[0]	BYTE0 ENABLE	O
129	EMI Interface	CPU_BE[1]	BYTE1 ENABLE	O
130	EMI Interface	CPU_RW	READ-NOT WRITE	O
131	EMI Interface	CPU_WATE	WATE STATE	I
132	EMI Interface	CPU_CE[4]	CHIP SEL.BANK3	O
133	EMI Interface	CPU_CE[3]	CHIP SEL.BANK2	O
134	EMI Interface	CPU_CE[2]	CHIP SEL.BANK1	O
135	EMI Interface	CPU_CE[0]	DARAM_RAS0	O
136	-	VDD3_3	3.3V POWER SUPPLY	POWER
137	-	VSS	GROUND	POWER
138	EMI Interface	CPU_RAS1	DARAM RAS	I/O
139	EMI Interface	CPU_CAS[0]	DARM CAS0	O
140	EMI Interface	CPU_CAS[1]	DRAM	O
141	EMI Interface	CPU_DATA[0]	DATA[0]	I/O
142	EMI Interface	CPU_DATA[1]	DATA[1]	I/O
143	EMI Interface	CPU_DATA[2]	DATA[2]	I/O
144	EMI Interface	CPU_DATA[3]	DATA[3]	I/O
145	EMI Interface	CPU_DATA[4]	DATA[4]	I/O
146	EMI Interface	CPU_DATA[5]	DATA[5]	I/O
147	EMI Interface	CPU_DATA[6]	DATA[6]	I/O
148	EMI Interface	CPU_DATA[7]	DATA[7]	I/O
149	-	VDD2_5	2.5V POWER SUPPLY	POWER
150	-	VSS	GROUND	POWER
151	EMI Interface	CPU_DATA[8]	DATA[8]	I/O
152	EMI Interface	CPU_DATA[9]	DATA[9]	I/O
153	EMI Interface	CPU_DATA[10]	DATA[10]	I/O
154	EMI Interface	CPU_DATA[11]	DATA[11]	I/O
155	EMI Interface	CPU_DATA[12]	DATA[12]	I/O
156	EMI Interface	CPU_DATA[13]	DATA[13]	I/O
157	EMI Interface	CPU_DATA[14]	DATA[14]	I/O
158	EMI Interface	CPU_DATA[15]	DATA[15]	I/O
159	-	VDD3_3	3.3V POWER SUPPLY	POWER
160	-	VSS	GROUND	POWER
161	EMI Interface	CPU_ADR[1]	ADR[1]	O
162	EMI Interface	CPU_ADR[2]	ADR[2]	O
163	EMI Interface	CPU_ADR[3]	ADR[3]	O
164	EMI Interface	CPU_ADR[4]	ADR[4]	O
165	EMI Interface	CPU_ADR[5]	ADR[5]	O
166	EMI Interface	CPU_ADR[6]	ADR[6]	O
167	EMI Interface	CPU_ADR[7]	ADR[7]	O
168	EMI Interface	CPU_ADR[8]	ADR[8]	O
169	EMI Interface	CPU_ADR[9]	ADR[9]	O
170	EMI Interface	CPU_ADR[10]	ADR[10]	O
171	-	VDD2_5	2.5V POWER SUPPLY	POWER
172	-	VSS	GROUND	POWER
173	EMI Interface	CPU_ADR[11]	ADR[11]	O
174	EMI Interface	CPU_ADR[12]	ADR[12]	O

PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
175	EMI Interface	CPU_ADR[13]	ADR[13]	O
176	EMI Interface	CPU_ADR[14]	ADR[14]	O
177	EMI Interface	CPU_ADR[15]	ADR[15]	O
178	EMI Interface	CPU_ADR[16]	ADR[16]	O
179	EMI Interface	CPU_ADR[17]	ADR[17]	O
180	EMI Interface	CPU_ADR[18]	ADR[18]	O
181	EMI Interface	CPU_ADR[19]	ADR[19]	O
182	EMI Interface	CPU_ADR[20]	ADR[20]	O
183	EMI Interface	CPU_ADR[21]	ADR[21]	O
184	-	VDD3_3	3.3V POWER SUPPLY	POWER
185	-	VSS	GROUND	POWER
186	PIOs and communication	PIO0[0]	PIO0[0]	I/O
187	PIOs and communication	PIO0[1]	PIO0[1]	I/O
188	PIOs and communication	PIO0[2]	PIO0[2]	I/O
189	PIOs and communication	PIO0[3]	PIO0[3]	I/O
190	PIOs and communication	PIO0[4]	PIO0[4]	I/O
191	PIOs and communication	PIO0[5]	PIO0[5]	I/O
192	PIOs and communication	PIO0[6]	PIO0[6]	I/O
193	PIOs and communication	PIO0[7]	PIO0[7]	I/O
194	PIOs and communication	PIO1[0]	PIO1[0]	I/O
195	PIOs and communication	PIO1[1]	PIO1[1]	I/O
196	PIOs and communication	PIO1[2]	PIO1[2]	I/O
197	PIOs and communication	PIO1[3]	PIO1[3]	I/O
198	-	VDD2_5	2.5V POWER SUPPLY	POWER
199	-	VSS	GROUND	POWER
200	PIOs and communication	PIO1[4]	PIO1[4]	I/O
201	PIOs and communication	PIO1[5]	PIO1[5]	I/O
202	PIOs and communication	TRIGGER_IN	TRIGGER_IN FOR DCU	I/O
203	PIOs and communication	TRIGGER_OUT	TRIGGER_OUT FOR DCU	I/O
204	PIOs and communication	PIO2[0]	PIO2[0]	I/O
205	PIOs and communication	PIO2[1]	PIO2[1]	I/O
206	PIOs and communication	PIO2[2]	PIO2[2]	I/O
207	PIOs and communication	PIO2[3]	PIO2[3]	I/O
208	PIOs and communication	PIO2[4]	PIO2[4]	I/O

IC TERMINAL DESCRIPTION

FUNCTIONAL BLOCK DIAGRAM



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Output terminal PC board

REF. NO.	PART NO.	DESCRIPTION
	ICs	
IC806	55124000NR	KIA7906API, Regulator
IC802	55128990NR	BA4560F, Op.amp.
IC801	55156030NR	PCM1748KE S, DAC
	Remote sensor	
IC805	J2123829001X	GP1F32T
	Transistors	
Q801,Q814	2097046092	KTC3875S
Q802	2097048092	KTA1504S Chip PNP
Q803L,Q803R	5503943092	DTC323T Chip NPN
Q805	5513318092	DTC114YKA NPN
Q804L	5513319092	DTA114YKA Chip PNP
Q803,Q804, Q807	5513869056	KTN2907A PNP
Q808,Q809, Q810-Q813	5513871092	KTD130 NPN
	Diodes	
D804	2049651091	1SS355 Chip
D805	7043654016	1N4148
	Coil inductors	
L805-L807	J2616227920X	LAL02 2.7uH
	Capacitors	
C807	20252680AM	100uF,10V+/-20%, 10 x 16
C809	1500213030	10uF,16V+/-20%,5 x 11
C857,C864, C869	2025265030	220uF,16V+/-20%, 8 x 11.5
C824,C860	2025267030	470uF,10V+/-20%,
C866	2025267030	
C802,C805, C811L,C811R, C816L,C816R, C819,C820	2026783030	47uF,16V+/-20%, 5 x 11
C836	2026894030	100uF,10V+/-20%, 5 x 11
C825	2026911030	330uF,6.3V+/-20%, 6.3 x 11
	Socket AS	
CN805	J4300200110X	12P 60MM

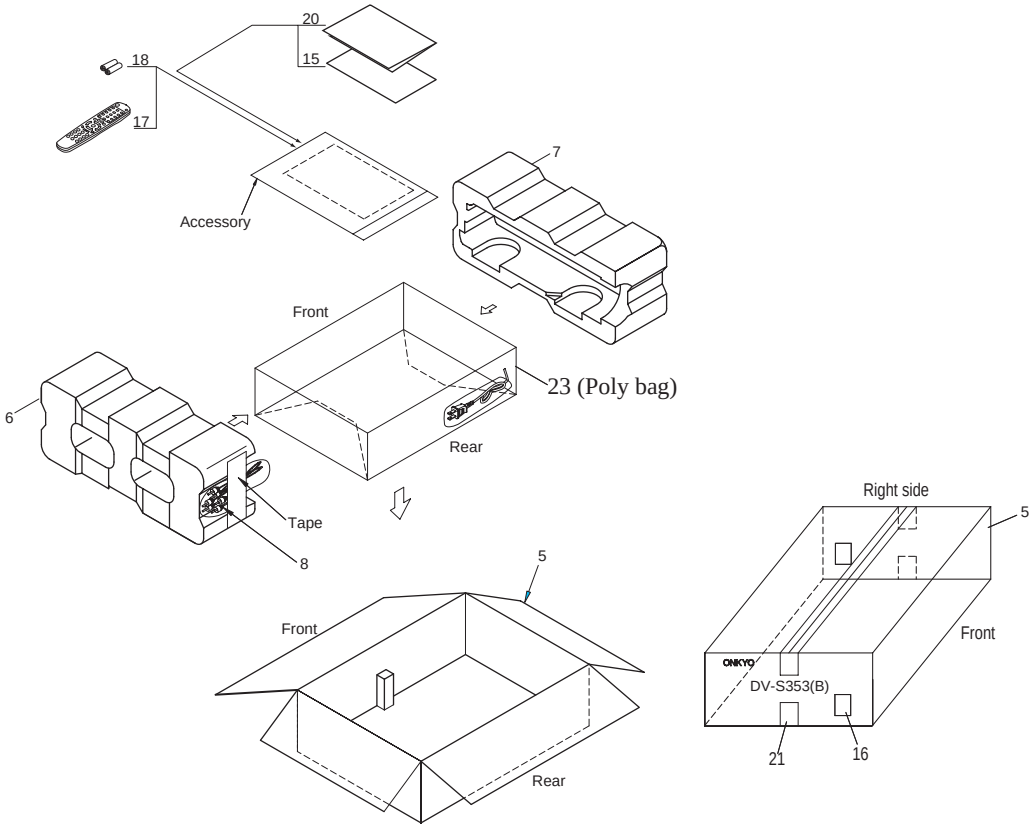
REF. NO.	PART NO.	DESCRIPTION
	Plugs	
CP803	55090070XX	
CP802	55124580XX	
CP801	55124630XX	
	Terminals	
JK801	J44301000900	RCA 1P
JK802	J44302000600	RCA 2P, JE020
JK804	J44303000200	RCA 3P RBG
JK803	J44312000100	RCA+S GNDCAP
	Wire buss	
L801,L804, L808	20441240XX	Wire buss 7.5mm
L803,L810, L811	20441210XX 20441240XX	Wire buss 20mm
L809	20441180XX	Wire buss 10mm

IC TERMINAL DESCRIPTION
CXP82532 (Microprocessor)

DV-S353 FRONT IC901 CXP82532 PIN LIST

PIN NUMBER	PIN NAME	MAIN FUNCTION	IN/OUT
1	PE3/INT3	INPUT FOR EXTERNAL INTERRUPT REQUEST.	I
2	PE4/RMC	INPUT FOR REMOTE CONTROL RECEIVING CIRCUIT.	I
3	PE5	-	I
4	PE6	-	O
5	PE7/TO	OUTPUT PIN FOR 16-BIT TIME/COUNTER RECTANGULAR WAVEFORM.	O
6	PBO/CINT	EXTERNAL CAPTURE INPUT FOR 16-BIT TIME/COUNTER.	I/O
7	PBI/'CSO	CHIP SELLECT INPUT FOR SERIAL INTERFACE(CH0).	I/O
8	PB2/'SCKO	SERIAL CLOCK (CH0) INPUT/OUTPUT.	I/O
9	PB3/SIO	SERIAL DATA (CH0) INPUT.	I/O
10	PB4/SOO	SERIAL DATA (CH0) OUTPUT.	I/O
11	PB5/'SCK1	SERIAL CLOCK (CH1) INPUT/OUTPUT.	I/O
12	PB6/SI1	SERIAL DATA (CH1) INPUT.	I/O
13	PB7/SO1	SERIAL DATA (CH1) OUTPUT.	O
14	PCO/KRO	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
15	PC1/KR1	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
16	PC2/KR2	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
17	PC3/KR3	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
18	PC4/KR4	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
19	PC5/KR5	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
20	PC6/KR6	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
21	PC7/KR7	KEY RETURN INPUT FOR FDP SEGMENT SIGNAL WITCH PERFORMS KEY SCANNING.	I/O
22	PA0/AN0	ANALOG INPUT TO A/D CONVERTER.	I/O
23	PA1/AN1	ANALOG INPUT TO A/D CONVERTER.	I/O
24	PA2/AN2	ANALOG INPUT TO A/D CONVERTER.	I/O
25	PA3/AN3	ANALOG INPUT TO A/D CONVERTER.	I/O
26	PA4/AN4	ANALOG INPUT TO A/D CONVERTER.	I/O
27	PA5/AN5	ANALOG INPUT TO A/D CONVERTER.	I/O
28	PA6/AN6	ANALOG INPUT TO A/D CONVERTER.	I/O
29	PA7/AN7	ANALOG INPUT TO A/D CONVERTER.	I/O
30	'RESET	SYSTEM RESET,ACTIVE "L"	I/O
31	EXTAL	CONNECTION FOR SYSTEM CLOCK OSCILLATION CRYSTAL.	I
32	XTAL	CONNECTION FOR SYSTEM CLOCK OSCILLATION CRYSTAL.	-
33	VSS	GND	-
34	PD0/S0	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
35	PD1/S1	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
36	PD2/S2	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
37	PD3/S3	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
38	PD4/S4	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
39	PD5/S5	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
40	PD6/S6	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
41	PD7/S7	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
42	PF0/S8	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
43	PF1/S9	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
44	PF2/S10	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
45	PF3/S11	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
46	PF4/S12	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
47	PF5/S13	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
48	PF6/S14	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
49	PF7/S15	8-BIT DEDICATED OUTPUT PORT/SEGMENT SIGNAL OUTPUT FOR FDP.	O
50	S16	SEGMENT SIGNAL OUTPUT FOR FDP.	O
51	S17	SEGMENT SIGNAL OUTPUT FOR FDP.	O
52	S18	SEGMENT SIGNAL OUTPUT FOR FDP.	O
53	S19	SEGMENT SIGNAL OUTPUT FOR FDP.	O
54	S20	SEGMENT SIGNAL OUTPUT FOR FDP.	O
55	T15/S21	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
56	T14/S22	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
57	T13/S23	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
58	T12/S24	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
59	T11/S25	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
60	T10/S26	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
61	T9/S27	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
62	T8/S28	DUAL PURPOSE OUTPUT FOR FDP TIMING AND SEGMENT SIGNAL.	O
63	T7	TIMING SIGNAL OUTPUT FOR FDP	O
64	T6	TIMING SIGNAL OUTPUT FOR FDP	O
65	T5	TIMING SIGNAL OUTPUT FOR FDP	O
66	T4	TIMING SIGNAL OUTPUT FOR FDP	O
67	T3	TIMING SIGNAL OUTPUT FOR FDP	O
68	T2	TIMING SIGNAL OUTPUT FOR FDP	O
69	T1	TIMING SIGNAL OUTPUT FOR FDP	O
70	T0	TIMING SIGNAL OUTPUT FOR FDP	O
71	VFDP	PROVIDES VOLTAGE FOR FDP	-
72	VDD	POSITIVE POWER SUPPLY PIN.	-
73	VPP	POSITIVE POWER SUPPLY FOR THE PROGRAMMABLE ON-CHIP PROM.	-
74	PG0	4-BIT IN/OUTPUT PORT;SINGLE BIT ADDRESSABLE	I/O
75	PG1	4-BIT IN/OUTPUT PORT;SINGLE BIT ADDRESSABLE	I/O
76	PG2	4-BIT IN/OUTPUT PORT;SINGLE BIT ADDRESSABLE	I/O
77	PG3	4-BIT IN/OUTPUT PORT;SINGLE BIT ADDRESSABLE	I/O
78	PE0/'EC0/INT0	INPUT FOR EXTERNAL INTERRUPT REQUEST.	I
79	PE1/'EC1/INT1	INPUT FOR EXTERNAL INTERRUPT REQUEST.	I
80	PE2/IN2	INPUT FOR EXTERNAL INTERRUPT REQUEST.	I

PACKING VIEW/ PARTS LIST



REF. NO.	PART NO.	DESCRIPTION
5	J96000041000	Carton box
6	J97200046010	Pad R
7	J97200046020	Pad L
8	J94330001000	3P1500, Audio connection cable with poly cover
10	J97000501010	Poly bag, Power cord with poly cover
11	J97000002000	Poly bag
12	20194780XX	Cable tie
14	J94310007000	1P 01000, Pin cord
15	J90300022000	Warrany card
16	J90400048000	POS label
17	55167260XX	Remote controller RC-458DV with poly cover
18	3010054	Battery UM3
20	J90200021000	Instruction manual
21	29110141	PP tape
22	29100097-1A	Poly bag 350 x 250
23	29100037-1A	Poly bag 650 x 500

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PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
58	Share memory interface	SMI_ADR[4]	ADDRESS BUS SDRAM	O
59	Share memory interface	SMI_ADR[5]	ADDRESS BUS SDRAM	O
60	Share memory interface	SMI_ADR[6]	ADDRESS BUS SDRAM	O
61	Share memory interface	SMI_ADR[7]	ADDRESS BUS SDRAM	O
62	Share memory interface	SMI_ADR[8]	ADDRESS BUS SDRAM	O
63	Share memory interface	SMI_ADR[9]	ADDRESS BUS SDRAM	O
64	-	VDD2_5	2.5V POWER SUPPLY	POWER
65	-	VSS	GROUND	POWER
66	Share memory interface	SMI_ADR[0]	ADDRESS BUS SDRAM	O
67	Share memory interface	SMI_ADR[1]	ADDRESS BUS SDRAM	O
68	Share memory interface	SMI_ADR[2]	ADDRESS BUS SDRAM	O
69	Share memory interface	SMI_ADR[3]	ADDRESS BUS SDRAM	O
70	Share memory interface	SMI_ADR[10]	ADDRESS BUS SDRAM	O
71	Share memory interface	SMI_ADR[11]	ADDRESS BUS SDRAM	O
72	Share memory interface	SMI_ADR[12]	ADDRESS BUS SDRAM	O
73	Share memory interface	SMI_ADR[13]	ADDRESS BUS SDRAM	O
74	Share memory interface	SMI_CS[0]	CHIP SELECT BANK0	O
75	Share memory interface	SMI_CS[1]	CHIP SELECT BANK1	O
76	Share memory interface	SMI_RAS	RAS SDRAM	O
77	Share memory interface	SMI_CAS	CAS SDRAM	O
78	Share memory interface	SMI_WE	SDRAM WRITE ENABLE	O
79	Share memory interface	SMI_DQML	DQ MASK EN LOW	O
80	Share memory interface	SMI_DQMU	DQ MASK EN UP	O
81	-	VDD3_3	3.3V POWER SUPPLY	POWER
82	Share memory interface	SMI_CLKIN	SDRAM CLOCK IN	O
83	-	VSS	GROUND	POWER
84	Share memory interface	SMI_DATA[0]	DATA BUS SDRAM	I/O
85	Share memory interface	SMI_DATA[1]	DATA BUS SDRAM	I/O
86	Share memory interface	SMI_DATA[2]	DATA BUS SDRAM	I/O
87	Share memory interface	SMI_DATA[3]	DATA BUS SDRAM	I/O
88	Share memory interface	SMI_DATA[4]	DATA BUS SDRAM	I/O
89	Share memory interface	SMI_DATA[5]	DATA BUS SDRAM	I/O
90	Share memory interface	SMI_DATA[6]	DATA BUS SDRAM	I/O
91	Share memory interface	SMI_DATA[7]	DATA BUS SDRAM	I/O
92	Share memory interface	SMI_DATA[8]	DATA BUS SDRAM	I/O
93	Share memory interface	SMI_DATA[9]	DATA BUS SDRAM	I/O
94	-	VDD2_5	2.5V POWER SUPPLY	POWER
95	Share memory interface	SMI_CLKOUT	SDRAM CLOCK OUT	O
96	-	VSS	GROUND	POWER
97	Share memory interface	SMI_DATA[10]	DATA BUS SDRAM	I/O
98	Share memory interface	SMI_DATA[11]	DATA BUS SDRAM	I/O
99	Share memory interface	SMI_DATA[12]	DATA BUS SDRAM	I/O
100	Share memory interface	SMI_DATA[13]	DATA BUS SDRAM	I/O
101	Share memory interface	SMI_DATA[14]	DATA BUS SDRAM	I/O
102	Share memory interface	SMI_DATA[15]	DATA BUS SDRAM	I/O
103	RESERVED	RESERVED		I/O
104	RESERVED	RESERVED		I/O
105	RESERVED	RESERVED		I/O
106	RESERVED	RESERVED		O
107	-	VDD3_3	3.3V POWER SUPPLY	POWER
108	-	VSS	GROUND	POWER
109	JTAG	TRAT4	TEST RESET	I
110	JTAG	TMS	TEST MODE SELECT	I
111	JTAG	TDO	TEAT DATA OUT	O
112	JTAG	TDI	TEST DATA IN	I
113	JTAG	TCK	TEST CLOCK	I
114	TIMER	PWM2	PULSE WITDH MODULA2	I/O
115	TIMER	PWM1	PULSE WITDH MODULA1	I/O
116	TIMER	PWM0	PULSE WITDH MODULA0	I/O

PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
117	EMI Interface	CPU_OE	OUTPUT_ENABLE	I/O
118	EMI Interface	CPU_RAM_CLK	SDRAM CLOCK	O
119	-	VDD2_5	2.5V POWER SUPPLY	POWER
120	CLOCK & RESET	PIX_CLK	27 MHz main clock	I
121	-	VSS	GROUND	POWER
122	CLOCK & RESET	VDD_PLL	VDD PLL=2.5V	POWER 2.5V
123	CLOCK & RESET	VSS_PLL	GND PLL=GND	POWER GND
124	CLOCK & RESET	RESET	CHIP RESET	I
125	INTERRUPT	IRQ2[2]	IRQ[2](MD_IRQ)	I
126	INTERRUPT	IRQ2[1]	IRQ[1](ATAPI IRQ)	I
127	INTERRUPT	IRQ2[0]	IRQ[0](SERVO_IRQ)	I
128	EMI Interface	CPU_BE[0]	BYTE0 ENABLE	O
129	EMI Interface	CPU_BE[1]	BYTE1 ENABLE	O
130	EMI Interface	CPU_RW	READ-NOT WRITE	O
131	EMI Interface	CPU_WATE	WATE STATE	I
132	EMI Interface	CPU_CE[4]	CHIP SEL.BANK3	O
133	EMI Interface	CPU_CE[3]	CHIP SEL.BANK2	O
134	EMI Interface	CPU_CE[2]	CHIP SEL.BANK1	O
135	EMI Interface	CPU_CE[0]	DARAM_RAS0	O
136	-	VDD3_3	3.3V POWER SUPPLY	POWER
137	-	VSS	GROUND	POWER
138	EMI Interface	CPU_RAS1	DARAM RAS	I/O
139	EMI Interface	CPU_CAS[0]	DARM CAS0	O
140	EMI Interface	CPU_CAS[1]	DRAM	O
141	EMI Interface	CPU_DATA[0]	DATA[0]	I/O
142	EMI Interface	CPU_DATA[1]	DATA[1]	I/O
143	EMI Interface	CPU_DATA[2]	DATA[2]	I/O
144	EMI Interface	CPU_DATA[3]	DATA[3]	I/O
145	EMI Interface	CPU_DATA[4]	DATA[4]	I/O
146	EMI Interface	CPU_DATA[5]	DATA[5]	I/O
147	EMI Interface	CPU_DATA[6]	DATA[6]	I/O
148	EMI Interface	CPU_DATA[7]	DATA[7]	I/O
149	-	VDD2_5	2.5V POWER SUPPLY	POWER
150	-	VSS	GROUND	POWER
151	EMI Interface	CPU_DATA[8]	DATA[8]	I/O
152	EMI Interface	CPU_DATA[9]	DATA[9]	I/O
153	EMI Interface	CPU_DATA[10]	DATA[10]	I/O
154	EMI Interface	CPU_DATA[11]	DATA[11]	I/O
155	EMI Interface	CPU_DATA[12]	DATA[12]	I/O
156	EMI Interface	CPU_DATA[13]	DATA[13]	I/O
157	EMI Interface	CPU_DATA[14]	DATA[14]	I/O
158	EMI Interface	CPU_DATA[15]	DATA[15]	I/O
159	-	VDD3_3	3.3V POWER SUPPLY	POWER
160	-	VSS	GROUND	POWER
161	EMI Interface	CPU_ADR[1]	ADR[1]	O
162	EMI Interface	CPU_ADR[2]	ADR[2]	O
163	EMI Interface	CPU_ADR[3]	ADR[3]	O
164	EMI Interface	CPU_ADR[4]	ADR[4]	O
165	EMI Interface	CPU_ADR[5]	ADR[5]	O
166	EMI Interface	CPU_ADR[6]	ADR[6]	O
167	EMI Interface	CPU_ADR[7]	ADR[7]	O
168	EMI Interface	CPU_ADR[8]	ADR[8]	O
169	EMI Interface	CPU_ADR[9]	ADR[9]	O
170	EMI Interface	CPU_ADR[10]	ADR[10]	O
171	-	VDD2_5	2.5V POWER SUPPLY	POWER
172	-	VSS	GROUND	POWER
173	EMI Interface	CPU_ADR[11]	ADR[11]	O
174	EMI Interface	CPU_ADR[12]	ADR[12]	O

PIN NUMBER	DESCRIPTION	PIN NAME	MAIN FUNCTION	TYPE
175	EMI Interface	CPU_ADR[13]	ADR[13]	O
176	EMI Interface	CPU_ADR[14]	ADR[14]	O
177	EMI Interface	CPU_ADR[15]	ADR[15]	O
178	EMI Interface	CPU_ADR[16]	ADR[16]	O
179	EMI Interface	CPU_ADR[17]	ADR[17]	O
180	EMI Interface	CPU_ADR[18]	ADR[18]	O
181	EMI Interface	CPU_ADR[19]	ADR[19]	O
182	EMI Interface	CPU_ADR[20]	ADR[20]	O
183	EMI Interface	CPU_ADR[21]	ADR[21]	O
184	-	VDD3_3	3.3V POWER SUPPLY	POWER
185	-	VSS	GROUND	POWER
186	PIOs and communication	PIO0[0]	PIO0[0]	I/O
187	PIOs and communication	PIO0[1]	PIO0[1]	I/O
188	PIOs and communication	PIO0[2]	PIO0[2]	I/O
189	PIOs and communication	PIO0[3]	PIO0[3]	I/O
190	PIOs and communication	PIO0[4]	PIO0[4]	I/O
191	PIOs and communication	PIO0[5]	PIO0[5]	I/O
192	PIOs and communication	PIO0[6]	PIO0[6]	I/O
193	PIOs and communication	PIO0[7]	PIO0[7]	I/O
194	PIOs and communication	PIO1[0]	PIO1[0]	I/O
195	PIOs and communication	PIO1[1]	PIO1[1]	I/O
196	PIOs and communication	PIO1[2]	PIO1[2]	I/O
197	PIOs and communication	PIO1[3]	PIO1[3]	I/O
198	-	VDD2_5	2.5V POWER SUPPLY	POWER
199	-	VSS	GROUND	POWER
200	PIOs and communication	PIO1[4]	PIO1[4]	I/O
201	PIOs and communication	PIO1[5]	PIO1[5]	I/O
202	PIOs and communication	TRIGGER_IN	TRIGGER_IN FOR DCU	I/O
203	PIOs and communication	TRIGGER_OUT	TRIGGER_OUT FOR DCU	I/O
204	PIOs and communication	PIO2[0]	PIO2[0]	I/O
205	PIOs and communication	PIO2[1]	PIO2[1]	I/O
206	PIOs and communication	PIO2[2]	PIO2[2]	I/O
207	PIOs and communication	PIO2[3]	PIO2[3]	I/O
208	PIOs and communication	PIO2[4]	PIO2[4]	I/O